The Definitive Guide to ARM Cortex -M0 and Cortex-M0+ Processors (2nd Revised edition)

By Joseph Yiu

Elsevier Science Publishing Co Inc. Paperback. Book Condition: new. BRAND NEW, The Definitive Guide to ARM Cortex -M0 and Cortex-M0+ Processors (2nd Revised edition), Joseph Yiu, The Definitive Guide to the ARM Cortex -M0 and Cortex-M0+ Processors, Second Edition explains the architectures underneath ARM's Cortex-M0 and Cortex-M0+ processors and their programming techniques. Written by ARM's Senior Embedded Technology Manager, Joseph Yiu, the book is packed with examples on how to use the features in the Cortex-M0 and Cortex-M0+ processors. It provides detailed information on the instruction set architecture, how to use a number of popular development suites, an overview of the software development flow, and information on how to locate problems in the program code and software porting. This new edition includes the differences between the Cortex-M0 and Cortex-M0+ processors such as architectural features (e.g. unprivileged execution level, vector table relocation), new chapters on low power designs and the Memory Protection Unit (MPU), the benefits of the Cortex-M0+ processor, such as the new single cycle I/O interface, higher energy efficiency, better performance and the Micro Trace Buffer (MTB) feature, updated software development tools, updated Real Time Operating System examples using KeilT RTX with CMSIS-RTOS APIs, examples of using various Cortex-M0...

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