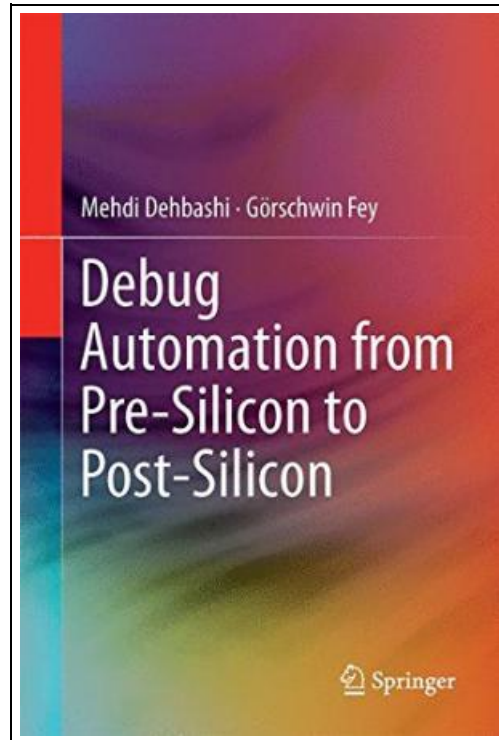# Debug Automation from Pre-Silicon to Post-Silicon

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Reviews

This written publication is fantastic. I am quite late in start reading this one, but better then never. You will not feel monotony at at any time of your respective time (that's what catalogues are for concerning should you ask me).

(Tevin McClure)

DEBUG AUTOMATION FROM PRE-SILICON TO POST-SILICON

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Springer-Verlag Gmbh Okt 2014, 2014. Buch. Condition: Neu. Neuware - This book describes automated debugging approaches for the bugs and the faults which appear in different abstraction levels of a hardware system. The authors employ a transaction-based debug approach to systems at the transaction-level, asserting the correct relation of transactions. The automated debug approach for design bugs finds the potential fault candidates at RTL and gate-level of a circuit. Debug techniques for logic bugs and synchronization bugs are demonstrated, enabling readers to localize the most difficult bugs. Debug automation for electrical faults (delay faults)finds the potentially failing speedpaths in a circuit at gate-level. The various debug approaches described achieve high diagnosis accuracy and reduce the debugging time, shortening the IC development cycle and increasing the productivity of designers. Describes a unified framework for debug automation used at both pre-silicon and post-silicon stages; Provides approaches for debug automation of a hardware system at different levels of abstraction, i.e., chip, gate-level, RTL and transaction level; Includes techniques for debug automation of design bugs and electrical faults, as well as an infrastructure to debug NoC-based multiprocessor SoCs. 171 pp. Englisch.

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